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Applicant: THIBEAULT et al.

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Examiner's Initials*	Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
<i>QG</i>	AR 5,101,347	03/1992	BALAKRISHNAN et al.			
<i>QG</i>	BR 5,815,031	09/1998	TAN et al.			
<i>QG</i>	CR 5,886,943	03/1999	SEKIGUCHI et al.			
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<i>QG</i>	FR 5,994,946	11/1999	ZHANG			
<i>QG</i>	GR 6,008,705	12/1999	GHOSHAL			
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FOREIGN PATENT DOCUMENTS

Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract	Translation Readily Available
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OTHER (Including in this order Author, Title, Periodical Name, Date, Pertinent Pages, etc.)

<i>QG</i>	MR	Ismail et al., Repeater Insertion in Tree Structured Inductive Interconnect, Proceedings of the 1999 International Conference on Computer-aided Design, November 1999, pp. 420-424.				
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<i>QG</i>	TR	Sato et al., A 5-Gbyte/s Data-Transfer Scheme With Bit-to-Bit Skew Control For Synchronous DRAM, IEEE Journal of Solid State Circuits, vol. 34, No. 5, May 1999, pp. 653-660.				

Examiner

Date Considered: 7/14/05

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.